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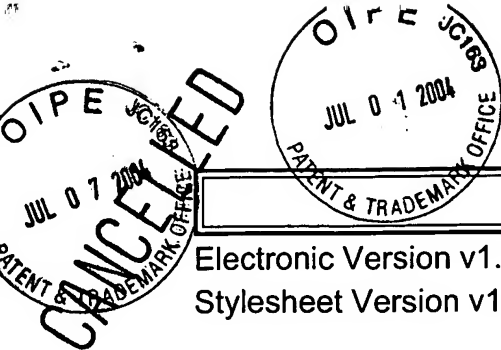


Electronic Filing System (EFS) Data  
Electronic Patent Application Submission  
USPTO Use Only

EFS ID: 63791  
Application ID: 10815505   
Title of Invention: Integrated Circuit Devices Having  
Data Inversion Circuits Therein  
with Multi-Bit Prefetch Structures  
and Methods of Operating Same  
First Named Inventor: Min-sang Park  
Domestic/Foreign Application: Domestic Application  
Filing Date: 2004-04-01  
Effective Receipt Date: 2004-07-01  
Submission Type: Information Disclosure  
Statement  
Filing Type:  
Confirmation number: 1295  
Attorney Docket Number: 5649-1100IP


Total Fees Authorized:

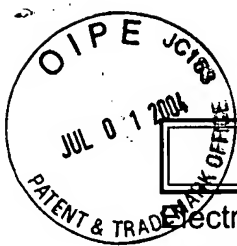
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## TRANSMITTAL

Electronic Version v1.1  
Stylesheet Version v1.1.0


<b>Title of Invention</b>	Integrated Circuit Devices Having Data Inversion Circuits Therein with Multi-Bit Prefetch Structures and Methods of Operating Same									
<p>Application Number: 10/815505 </p> <p>Date: 2004-04-01</p> <p>First Named Applicant: Min-sang Park</p> <p>Confirmation Number: 1295</p> <p>Attorney Docket Number: 5649-1100IP</p>										
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<table border="1"><thead><tr><th>Submitted by:</th><th>Elec. Sign.</th><th>Sign. Capacity</th></tr></thead><tbody><tr><td>Grant J. Scott Registered Number: 36925</td><td>/gjs/</td><td>Attorney</td></tr></tbody></table>			Submitted by:	Elec. Sign.	Sign. Capacity	Grant J. Scott Registered Number: 36925	/gjs/	Attorney		
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Comments										



## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

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